

10/064306

**PATENT NUMBER and
ISSUE DATE**

U.S. UTILITY Patent Application

APPL NUM 10064306	FILING DATE 07/01/2002	CLASS 24	SUBCLASS 205	GAU 2186	EXAMINER AUDUONG
**APPLICANTS: Brennan Ciaran, Fifield John, Stephens Jeremy, Storaska Daniel;					
365 7818					
**CONTINUING DATA VERIFIED: <i>None</i>					
** FOREIGN APPLICATIONS VERIFIED: <i>None</i>					
PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>			
Foreign priority claimed 35 USC 119 conditions met Verified and Acknowledged Examiner's initials			<input checked="" type="checkbox"/> yes <input type="checkbox"/> no	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no	ATTORNEY DOCKET NO FIS920010334
TITLE : Writeback and refresh circuitry for direct sensed DRAM macro					
U.S. DEPT. OF COMM. FAF-8 TM-PTD-436(Rev. 10-94)					

NOTICE OF ALLOWANCE MAILED		Assistant Examiner	CLAIMS ALLOWED		
			Total Claims 0.0	Print Claim for	
ISSUE FEE		DRAWING			
Amount Due	Date Paid	Sheets Drawn	Figs. Drawn	Print Fig.	
		Primary Examiner			
<input type="checkbox"/> TERMINAL DISCLAIMER		PREPARED FOR ISSUE			Application Examiner
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